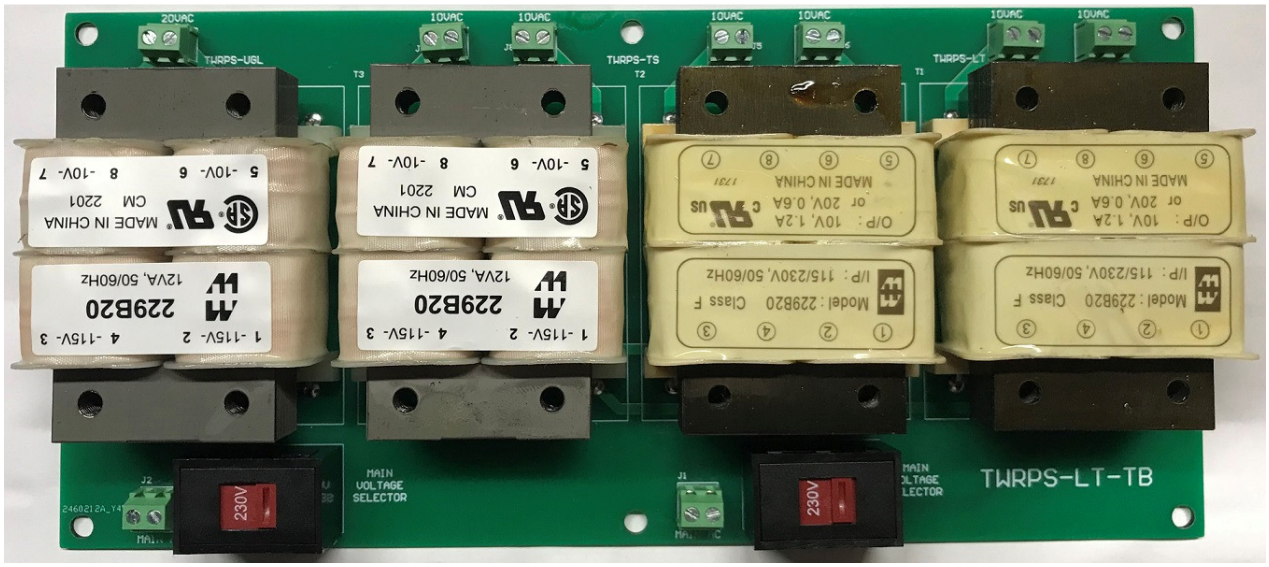
TWRPS-LT-TB Transformers bank



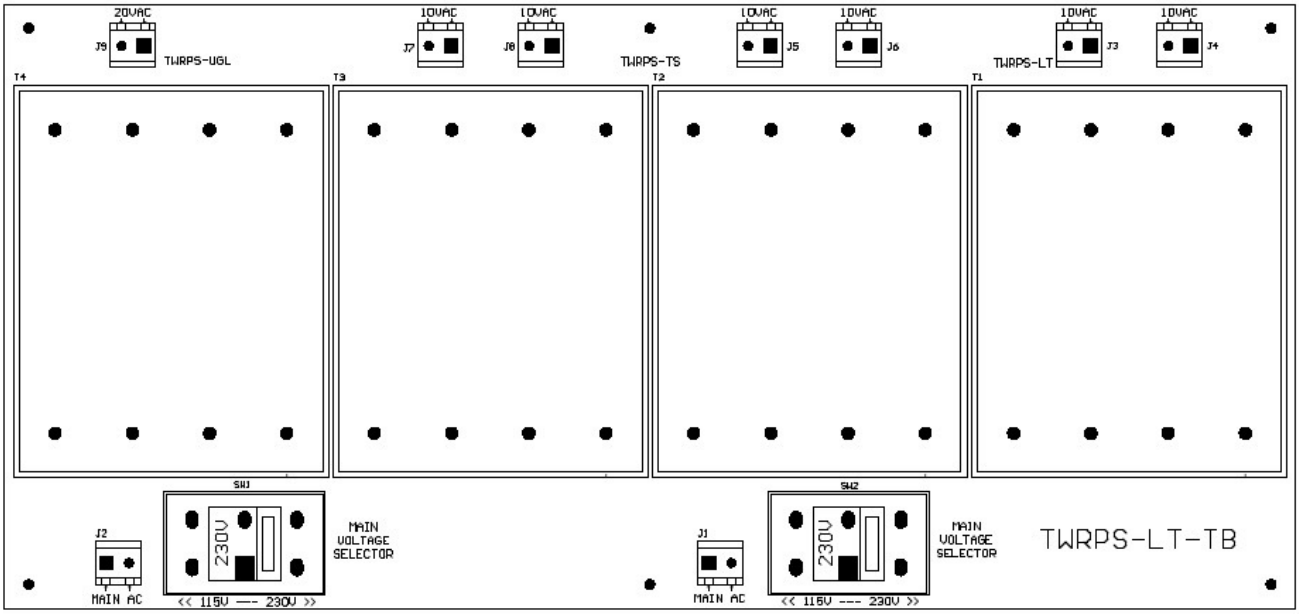
The TWRPS-LT-TB board has been designed to provide AC power supply to the following regulators:

- TWRPS-UGL unity gain linear regulator for DRIXO/EXO and PXO oscillators
- TWRPS-TS voltage tracking shunt regulator for TWSDAC-LT DAC Lite or TWRPS-SS shunt regulator for TWSDAC-DSD discrete DAC
- TWRPS-LT shunt regulator for TWSAFB-LT FIFO buffer

Features:

Inputs: selectable 115/230 VAC
Output: 20VAC, 10VAC x 6
Board size: 217 x 77 mm

PCB layout



Connectors

J1: 115/230 main AC

J2: 115/230 main AC

J3: 10 VAC connect to J11 of TWRPS-LT

J4: 10 VAC connect to J51 of TWRPS-LT

J5: 10 VAC connect to J2 of TWRPS-TS or J1 of TWRPS-SS

J6: 10 VAC connect to J3 of TWRPS-TS or J11 of TWRPS-SS

J7: 10 VAC connect to J1 of TWRPS-TS

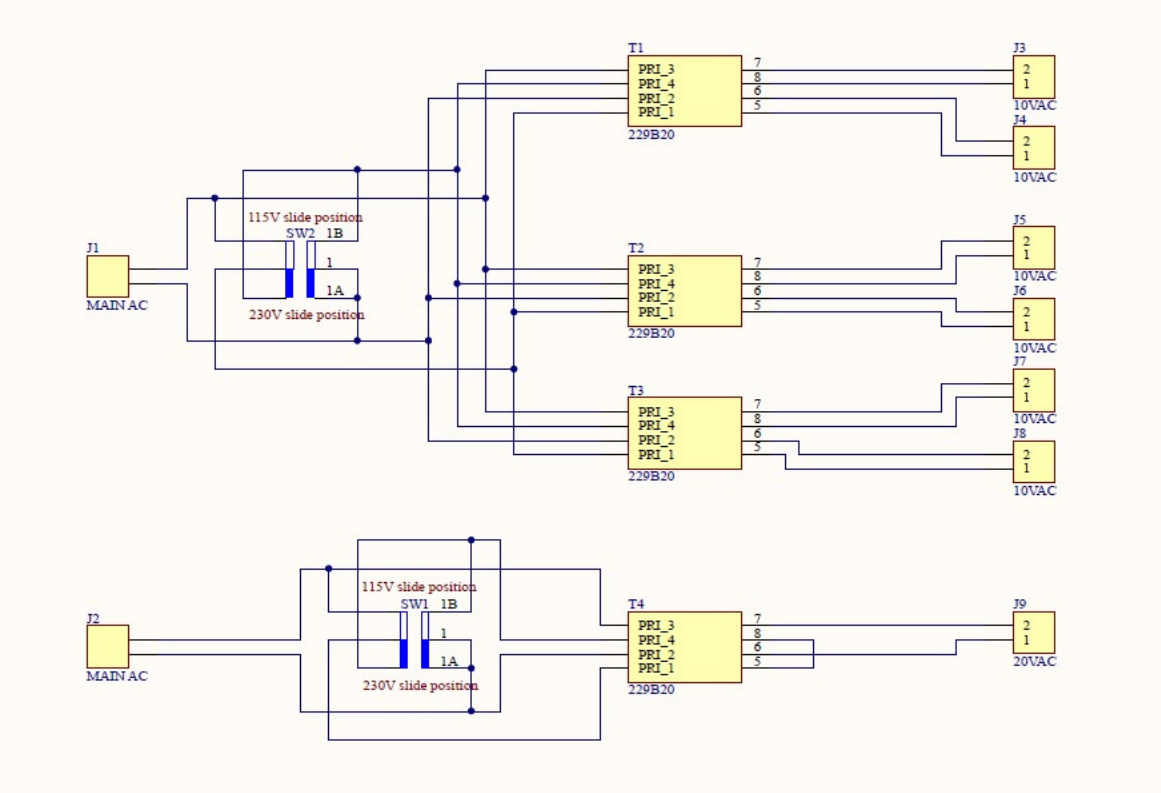
J8: 10 VAC connect to J11 of TWRPS-TS

J9: 20 VAC connect to XFMR SEC of TWRPS-UGL

There is 1 available option for this board:

- bare PCB (all parts are TH)

Schematic



Notes on bare board

The bare board needs all the parts to be soldered (all TH parts).

There are 2 separate main AC connectors and main AC selectors:

1. J1 and SW1 provide AC for TWRPS-LT and TWRPS-TS or TWRPS-SS
2. J2 and SW2 provide AC for TWRPS-UGL

This allows to switch off FIFO Lite and DAC (J1) while oscillators remain powered (J2).

There are a few things to pay the maximum attention:

- be careful installing connectors and polarized parts with the right orientation, the component orientation is clearly visible on the PCB overlay

Settings

Select the appropriate main AC according to your AC provider by the slide selectors SW1 and SW2.